

EAST - [10.628517.wsp:1] [] [] []

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81: (7) stubless adj2 configur\$6
86: (6) stubless near5 configur\$6
80: (461) point-to-point adj2 configur\$6
810: (43666) memory adj3 (module or board)
814: (44) 89 and 810
816: (297) stub adj2 configur\$6
817: (21) 810 and 816
821: (934837) terminat\$6
822: (1157) 810 same 821
823: (1159) 366/62
824: (3194) 366/63
825: (60301) "366"/\$6
826: (1664) 361/728
827: (706) 361/729
829: (7) 822 and 826
828: (4) 822 and 827
830: (62) 822 and 823
831: (87) 822 and 824
832: (342) 822 and 826
834: (26) (FUNABA near2 SEIJI).in.
835: (236) (NISHIO near2 YOJI).in.
836: (26) (SHIBATA near2 KAYOKO).in.

Search Groups Bulk
 DB: US-PPUB;USPAT;EPO;JPO;DERWENT;IBM_TOB
 Default operator: OR Highlight all items initially

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U	K	Document ID	Inventor	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	S	C	P	O
1	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6661690 B2	Moriarty; Sharon L. et al.	20031209	19	High capacity memory module	366/63	366/52	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6142830 A	Loeffler; Steffen	20001107	7	Signaling improvement using	439/620	361/788	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6590781 B2	Kollipara; Ravindranath	20030708	28	Clock routing in multiple	361/760	174/250;	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6742098 B1	Halbert; John B. et al.	20040626	18	Dual-port buffer-to-memory	711/172	366/230.05;	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6772262 B1	Park; Myun-joo et al.	20040803	17	Memory module with improved	710/301	361/736;	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6438012 B1	Osaka; Hideki et al.	20020820	22	Directional coupling memory	366/52	366/230.03	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6392837 B1	Nakase; Yasunobu et al.	20020521	31	Circuit module	361/785	228/180.1;	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6619173 B2	Funaba; Seiji et al.	20030211	51	Memory system	366/63	366/99	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6172895 B1	Brown; Dirk D. et al.	20010109	14	High capacity memory module	366/63	366/52	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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